

**Notice of References Cited**

Application/Control No.

09/838,084

Applicant(s)/Patent Under  
Reexamination  
LO ET AL.

Examiner

Suk-San Foong

Art Unit

2823

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,265,283	07-2001	Nariman et al.	438/424
	B	US-6,159,823	12-2000	Song et al.	438/437
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ghandhi S. "VLSI Fabrication Principles: Silicon and Gallium Arsenide", 1983, John Wiley & Sons, Inc., p. 614
	V	Wolf S. "Silicon Processing for the VLSI-ERA: Volume 1-Process Technology", 1986, Lattice Pr., Vol. 1, p. 407, 476
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.